# Year End 2020 DASC Report

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**Design Automation Standards Committee**
**11/2020**

# Introduction to the DASC

- One of the oldest sponsor groups
  - founded in 1987

- Sponsors standards in the Electronic Design Automation Area
  - Mainly SW for Electronics Design, but expanding in Adjacent areas

- 20 companies in the Electronics industry represented

- Meets one a month, once per year Face to Face

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# DASC and Other Standards Groups

- Continued cooperation with Accellera
  - 10 year "Get IEEE" contract funding
- Increased cooperation with Si2
  - Contributions from Si2 to P1801, P2416 and P2401
- Ongoing cooperation with JEITA
  - JEITA reps have joined DASC F2F meetings at DVCon for several years
  - DASC-JEITA joint meeting in Japan in November is now ongoing for 10 years
  - 2401 and 2804 have roots in Japan

# Officers– New as of November 2020!

- Chair Dennis Brophy, Mentor Graphics
- Vice Chair Yatin Trivedi, Aricent
- Secretary & Treasurer Aparna Dey, Cadence

# 2019-20 Highlights (1)

Standards Approved in 2019:
- 1076 (VHDL)
- 1481 (OLA)
- 1647 (e language)
- 1801 (UPF)
- 2401 (LSI-Package-Board)
- 2416 (System Level Low Power)
- 2804 (SHIM)

■ Standards Approved in 2020
- 1800.2  (UVM)

# 2019-20 Highlights (2)

- WGs Approved by NesCom in 2019:
  - P1666 (SystemC)
  - P1481 (OLA)
  - P1800 (SystemVerilog)
  - P1800.2 (UVM)
  - P2416 (System Level Low Power)
  - P2851 (Functional Safety)

# Other 2020 High Points

- "General Cooperation Agreement" between IEEE and Accellera signed
  - Allows parallel standards groups to meet together and share progress/documents without legal issues
- Currently applies to
  - IEEE P2851 and Accellera Function Safety WG
  - IEEE P1666 and Accellera SystemC Language WG
- Later may be applied to UVM and IP-XACT WGs

# 2020 Lowlights

- WGs that became Study Groups in 2020
  - P2415 (UHA)
- Standards that became "Inactive Reserved" in 2020
  - 1492-2010 (SDF)
  - 1850- 2010 (PSL)

# Current Published DASC Standards

| IEEE Standard # | Approved | IEC Dual Logo |
|---|---|---|
| 1076 VHDL | 2019 | D |
| 1076.1 (VHDL AMS) | 2017 | D |
| 1481 (OLA) | 2019 | D |
| 1647 (e language) | 2019 | |
| 1666 (SystemC) | 2011 | D |
| 1666.1 (SystemC AMS) | 2016 | D |
| 1685 (IP-XACT) | 2014 | D |
| 1734 (Quality IP) | 2011 | D |
| 1735 (IP Encryption) | 2015 | |
| 1800 (SystemVerilog) | 2017 | D |
| 1800.2 (UVM) | 2020 | D |
| 1801 (UPF) | 2018 | D |
| 2401 (LSI Package Board) | 2019 | D |
| 2416 (System Level Power) | 2019 | |
| 2804 (SHIM) | 2019 | D |

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# New DASC Standards in Development

| IEEE PAR # | PAR Approved | Ballot Expected |
|------------|--------------|-----------------|
| 2851 (Functional Safety) | 2019 | 2021 |

*In progress

# DASC Standards Being Revised

| IEEE PAR # | PAR Approved | Ballot Expected |
|---|---|---|
| P1666 (SystemC) | 2018 | 2021 |
| P1735 (IP encryption) | 2018 | 2020 |
| P1800 (SystemVerilog) | 2019 | 2022 |
| P1801 (UPF) | 2018 | 2022 |

\* Before RevCon November, 2019

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